IIB-429-MMA

ARINC-429 Controller M-module
2 Transmit and 2 Receive Channels

Functional Description

The IIB-429-MMA provides 4 ARINC-429 compatible channels (2 receive and 2 transmit) at any rate between 12.5 and 100 kbps.

Because the IIB-429-MMA is built upon a flexible platform, user functions can be implemented almost without restriction, making this product flexible and user-friendly.

Technical Features

➢ Flexible platform allows easy addition of user-specific requirements

➢ Two transmit and two receive channels

➢ Transmit and receive commands for specific labels

➢ 16 user selectable labels for interrupt generation upon reception

➢ Status and statistics monitoring for all channels

➢ Adjustable data rate from 12.5 to 100kbps

➢ ARINC-429 rise/fall capacitors selectable per channel

➢ Provides excellent platform for ARINC-429 protocol analysis

Technical Details

Controller:
The onboard MC68332 32-bit CPU executes firmware from local Flash memory, providing a versatile and user-friendly ARINC-429 interface

Receiving Data:
For each receive channel, a local buffer is available for each label. After its parity is checked, the received message is stored in the corresponding label buffer, overwriting the previous content. The last received message for a specific label can be retrieved, by issuing a command from the host to the firmware interface, specifying the channel and label required.

Transmitting Data:
A message is transmitted by passing it as a parameter to the command interface, and initiate the transmit by issuing the appropriate command. Parity generation and checking is done transparently to the user.

Interrupt Facilities:
The user can select up to 16 labels per channel for interrupt generation upon reception of new data. This feature provides a mechanism that allows a user to monitor a label without data-loss.
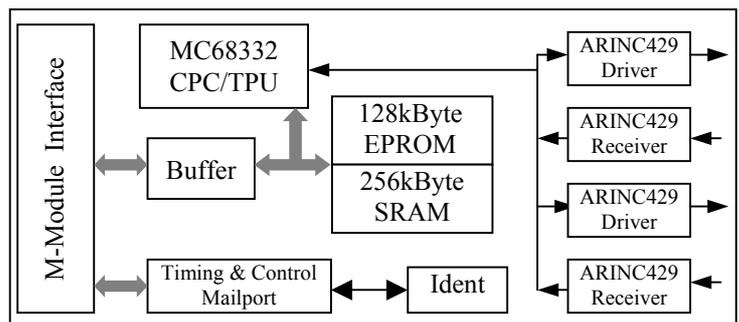
Statistics:
For each channel and for each label, the numbers of transmitted and received messages are logged into the statistics part of the local firmware. Parity errors are logged globally to a channel.

M-Module compliance:
A08/D16 (8-bit address and 16-bit data)
INTA (software end-of-interrupt)
IDENT (identification EEPROM)

Ordering Information

IIB-429-MMA/0T2R	IIB-429-MMA, no Xmit, 2x Recv
IIB-429-MMA/1T0R	IIB-429-MMA, 1x Xmit, no Recv
IIB-429-MMA/1T2R	IIB-429-MMA, 1x Xmit, 2x Recv
IIB-429-MMA/2T0R	IIB-429-MMA, 2x Xmit, no Recv
IIB-429-MMA/2T2R	IIB-429-MMA, 2x Xmit, 2x Recv
IIB-429-MMA/SW	APIS based software
IIB-429-MMA/MAN	IIB-429-MMA manual on paper