

IIB-3910-PC/AT

The IIB-3910-PC/AT is a single slot intelligent interface card providing complete STANAG 3838 and STANAG 3910 test, simulation and bus analysis capability for PC compatible host systems. The IIB-3910-PC/AT supports concurrent Bus Controller (BC) and up to 31 Remote Terminals (RT) with Bus Monitor (BM). An additional stand-alone Chronological Bus Monitor (CBM) facility is also provided, with powerful multi-level triggering capability. Full error injection capability in both 3838 and 3910 modes is provided in BC and RTs modes, with full error detection in BC, RT, BM and CBM modes. The unit provides a dual redundant 3838 interface, together with selectable dual redundant optical or single redundant electrical 3910 interface. 2 Mbytes of dual ported RAM are provided. PC memory space utilised by the board is selectable. The board is supplied with C drivers in source code and Windows menu driven software as standard.

SPECIFICATIONS

General Specifications

- ➔ STANAG 3838 compatible
- ➔ STANAG 3910 compatible
- ➔ PrEN 3715 compatible High Speed interface
- ➔ 2 Mbytes Dual Port RAM (all modes)
- ➔ Programmable interrupts
- ➔ BIT and diagnostics
- ➔ All set-ups programmable in real time
- ➔ 3838 programmable for Direct or TX coupling
- ➔ Full error injection (BC & RT)
- ➔ Full error detection (all modes)

Bus Controller

Programmable inter-message delay
Programmable RT response time
Programmable Frame frequency
Programmable message sequencing
Messages and data changeable in real time
Acyclic message insertion
BC-RT, RT-BC, RT-RT, mode code and broadcast.
Multiple data buffering for all transmitted and received messages
Full 3838 and 3910 error injection / detection

Remote Terminal

Multiple RT simulation (up to 31 RT's)
All sub-addresses selectable
Selectable Broadcast mode
Programmable Mode Code sub-address
Programmable Status word
Programmable RT response time
Message time tagging (32 bit @ 500 nSec)
All active RT tables changeable in real time
Data and Status words changeable in real time

Bus Monitor

All data buffers monitored
All data time tagged (32 bit @ 10 uSec resolution)
All data available for analysis

Chronological Bus Monitor

Programmable conditional triggers and masks
Message time tagging (32 bit @ 500 nSec resolution)
Store all, store only, store after modes
Full 3838 and 3910 error detection capability

Host Interface

PC compliance: AT, ISA, EISA Bus
Memory Space: Selectable as:
8, 16, 32, 64, 128, 256, 512Kbytes, 1, 2 Mbytes

Interrupts (4) user configurable to IRQ3 – 15

Power	+5v +/- 5%	1.5 Amps (electrical mode) 3.1 Amps (optical mode)
	+12V +/- 5%	50 mAmps
	-12V +/- 5%	160 mAmps

Physical Characteristics

Dimensions	270mm x 102mm
Weight	475 grams

Environmental

Operating Temp.	0 – 50°C
Storage Temp.	-20 – 70°C
Humidity	5% – 95% Noncondensing
MTBF (MIL 217F)	104,476 hrs at 25°C (GB).

Software Support

C drivers in source code
Windows menu software (Win3.x, WinNT, Win95)
LabWindows/CVI
LabVIEW
RT Validation Test Plan (full protocol)

Order Codes

1U10971G03	3838/3910 Optical/Electrical
1U10971G02	3838/3910 Electrical only
1U10971G01	3838/3910 Optical only

Contact Information

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